



AKSHAYA INSTITUTE OF TECHNOLOGY

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NBA Accredited (CSE)

Obalapura Post, Lingapura, Koratagere Road, Tumkur- 572 106, Karnataka



Department of Electronics & Communication Engineering

Report on three-days workshop on Chipcraft: VLSI Design Training Using Cadence EDA

Date: 21st August to 23rd August 2025

Akshaya Institute of Technology
Lingapura, Tumkur.

MeitY
Government of India

Department of Electronics & Communication Engineering
Organizing
Three Days Workshop On Chipcraft:
VLSI Design Training Using Cadence EDA
In Association with
Centre of Excellence In VLSI Design & MeitY
21st to 23rd August 2025

Coordinator: *Mrs. Bharathi N*
Convener: *Mrs. Padmavathi N*

Patrons:
Dr. Somashekhar G C, Program Head
Dr. Yathisha L., Principal

Chief Patrons:
Dr. Shivakumar K S, Chairman
Dr. Shobha S, Managing Trustee

Resource Person:

Mrs. Suchithra N P
Asst. Professor,
SKIT, Bengaluru

The Department of Electronics & Communication Engineering at Akshaya Institute of Technology, Lingapura, Tumkur, organized a three-day workshop on "Chipcraft: VLSI Design Training Using Cadence EDA" from August 21st to 23rd, 2025, in association with the Centre of Excellence in VLSI Design and MeitY, Government of India.

Workshop Overview:

The workshop provided participants with intensive and hands-on training in VLSI (Very-Large-Scale Integration) design, utilizing Cadence Electronic Design Automation (EDA) tools. The primary aim was to enhance practical knowledge and design skills in the field of semiconductor and integrated circuit design among students and faculty.

Organizing Team:

The workshop was coordinated by Mrs. Bharathi N and convened by Mrs. Padmavathi N. Patrons included Dr. Somashekhar G C (Program Head) and Dr. Yathisha L (Principal). Chief patrons were Dr. Shivakumar K S (Chairman) and Dr. Shobha S (Managing Trustee).

Resource Person:

The sessions were conducted by Mrs. Suchithra N P, Assistant Professor, SKIT Bengaluru, who delivered practical insights and demonstrations using Cadence EDA tools.

Key Highlights:

- Three days of interactive, hands-on sessions on VLSI design methodologies.
- Real-time design and simulation exercises with Cadence EDA.
- Insights into the latest trends in semiconductor technology.
- Platform for networking and collaboration between participants and experts.

Glimpses of the Event:



Tumakuru, Karnataka, India

C4fw+6x3, Tumakuru, Karnataka 572106, India
Lat 13.423034° Long 77.147766°
22/08/2025 10:34 AM GMT +05:30



Tumakuru, Karnataka, India

C4fw+6x3, Tumakuru, Karnataka 572106, India
Lat 13.422857° Long 77.147778°
22/08/2025 10:36 AM GMT +05:30







Conclusion

“Chipcraft: VLSI Design Training Using Cadence EDA” successfully equipped participants with valuable skills, preparing them for challenges in the semiconductor and electronics industries. The event also strengthened collaborations between academia and national initiatives striving for technological advancement in India.